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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

JC907 U.S.P.T.O.  
09/691004  
10/18/00

Patent Application of: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Attorney Docket No.: 303.324US4

**PATENT APPLICATION TRANSMITTAL**

**BOX PATENT APPLICATION**

Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

Return postcard.  
 CONTINUATION of prior Patent Application No. 08/903,452 (under 37 CFR § 1.53(b)) comprising:  
     Specification ( 32 pgs, including claims numbered 1 through 35 and a 1 page Abstract).  
     Formal Drawing(s) ( 11 sheets).  
     Signed Combined Declaration and Power of Attorney ( 6 pgs).  
     Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*  
     Check in the amount of \$2,800.00 to pay the filing fee.  
 Prior application is assigned of record to Micron Technology, Inc..  
 Information Disclosure Statement ( 1 pgs), Form 1449 ( 10 pgs). References NOT enclosed, cited in prior application.  
 Preliminary Amendment ( 16 pgs).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	65 - 20 =	45	x 18 =	\$810.00
INDEPENDENT CLAIMS	19 - 3 =	16	x 80 =	\$1,280.00
[ ] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$710.00
				<b>\$2,800.00</b>

Please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date of Deposit: October 18, 2000

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Box Patent Application, Washington, D.C. 20231.